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24309 7590 01/13/2006

XILINX, INC
 ATTN: LEGAL DEPARTMENT
 2100 LOGIC DR
 SAN JOSE, CA 95124

03/29/2006 HDEMESS2 00000003 240040 10603219

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Pat Tompkins		(Depositor's name)
<i>Pat Tompkins</i>		(Signature)
March 28, 2006		

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/603,219	06/24/2003	Anirban Rahut	X-1221 US	4298

TITLE OF INVENTION: UPPER-BOUND CALCULATION FOR PLACED CIRCUIT DESIGN PERFORMANCE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	04/13/2006
EXAMINER		ART UNIT	CLASS-SUBCLASS		
TO, TUYEN P		2825	716-013000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

"Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list
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1 Pablo Meles

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

XILINX, INC.

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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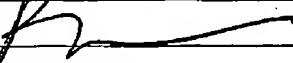
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Authorized Signature Date March 28, 2006Typed or printed name Kim KanzakiRegistration No. 37,652

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COMMISSIONER FOR PATENTS

P. O. Box 1450

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22313-1450

Inventor(s): Anirban Rahut et al.

Assignee: XILINX, INC.

Serial No.: 10/603,219

conf.no. 4298

Filed: June 24, 2003

Title: Upper-Bound Calculation for Placed Circuit Design Performance

Docket No.: X-1221 US

Enclosed: Return Receipt Postcard
Fee Transmittal - Part B

Date: March 28, 2006

VIA FACSIMILE 571-273-2885

Atty/Sec: KK/pat

